IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Applicants

M. Ueda

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MULTIPROCESSOR SYSTEM, PROCESSOR MODULE FOR USE THEREIN,

AND TASK ALLOCATION WETHOD IN MULTIPROCESSING

SEP 1 0 2001

Assistant Commissioner for Patents Washington, D. C. 20231

PRELIMINARY AMENDMENT

IN THE TITLE

In the title of the Specification, delete "MULTIPROCESSOR SYSTEM,
PROCESSOR MODULE FOR USE THEREIN, AND TASK ALLOCATION METHOD IN
MULTIPROCESSING", replace therefore -- PROCESSOR MODULE FOR A
MULTIPROCESSOR SYSTEM AND TASK ALLOCATION METHOD THEREOF-.

Please charge any fee necessary to enter this paper and any previous paper to deposit account 09-0468.

Respectfully submitted,

Derek S. Jennings

Reg. Patent Agent/Engineer

Reg. No.: 41,473

Tel. No.: (914) 945-2144

IBM Corporation
Intellectual Property Law Dept.
P. O. Box 218
Yorktown Heights, New York 10598